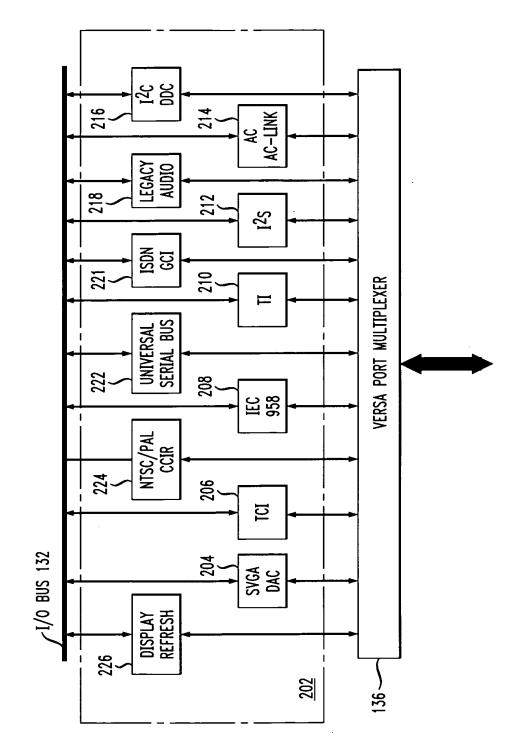
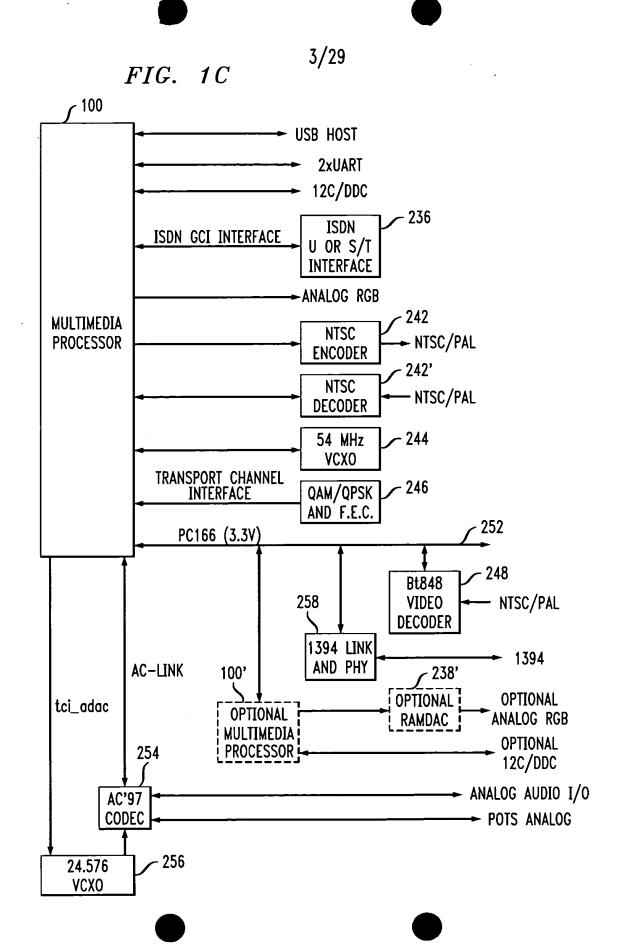
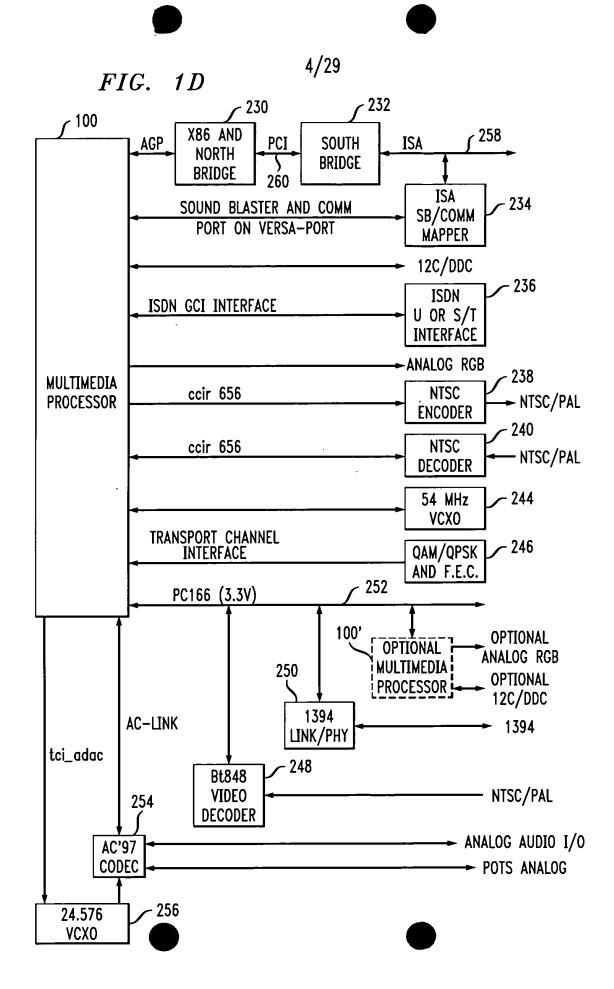
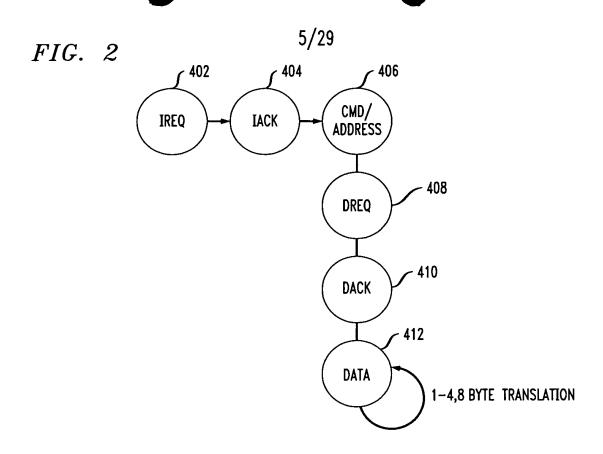


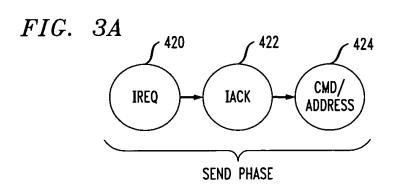
FIG. 1B

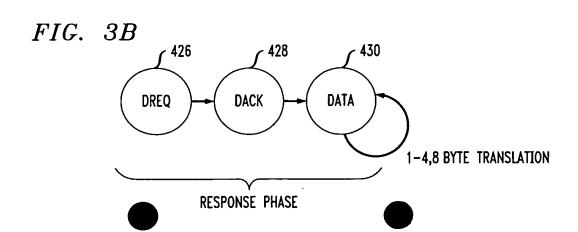












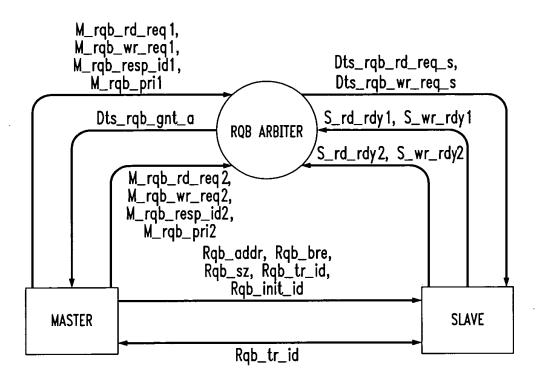


FIG. 4B

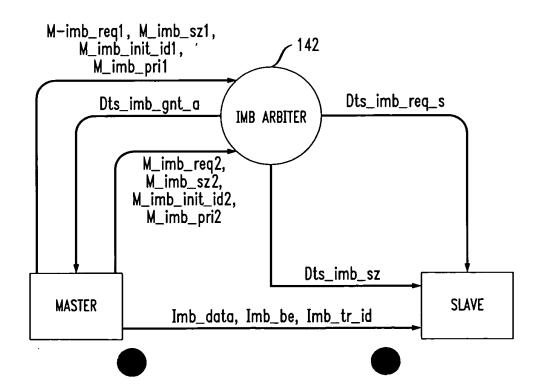
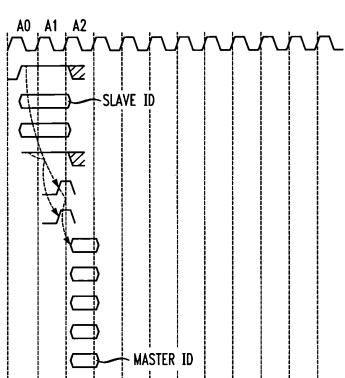


FIG. 5A

M_rqb_rd_req1
M_rqb_resp_id1
M_rqb_pri1
S_rd_rdy1
Dts_rqs_gnt_m
Dts_rqb_rd_req_s
Rqb_addr
Rqb_sz
Rqb_brs
Rqb_tr_id
Rqb_init_id



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FIG. 5B

M_rqb_rd_req1

M_rqb_resp_id1

M_rqb_pri1

S_rd_rdy1

Dts_rqs_gnt_m

Dts_rqb_rd_req_s

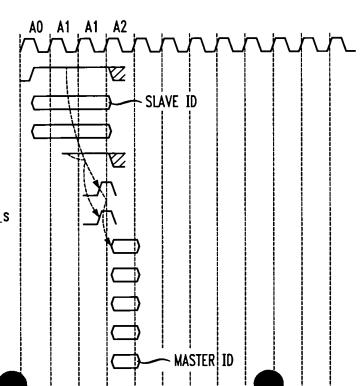
Rqb_addr

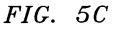
Rqb_sz

Rqb_brs

Rqb_tr_id

Rqb_init_id





M_rqb_wr_req1

M_rqb_resp_id1

 $Dts_rqb_gnt_m$

M_rqb_pri1

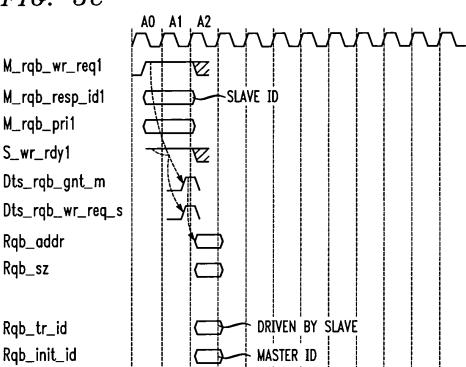
S_wr_rdy1

Rqb_addr

 Rqb_tr_id

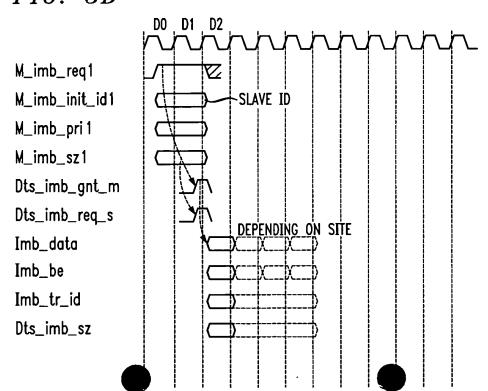
 ${\sf Rqb_init_id}$

 Rqb_sz

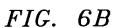


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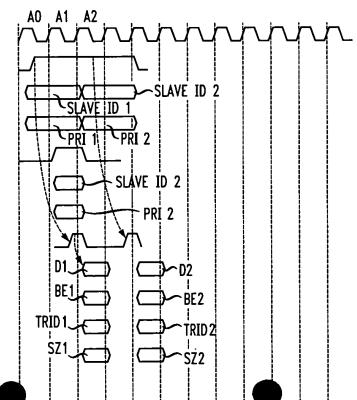
FIG. 5D



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M_pmb_req1
M_pmb_init_id1
M_pmb_pri1
M_pmb_req2
M_pmb_init_id2
M_pmb_pri2
Dts_pmb_gnt_m
Pmb_data
Pmb_be
Pmb_tr_id
Dts_pmb_sz



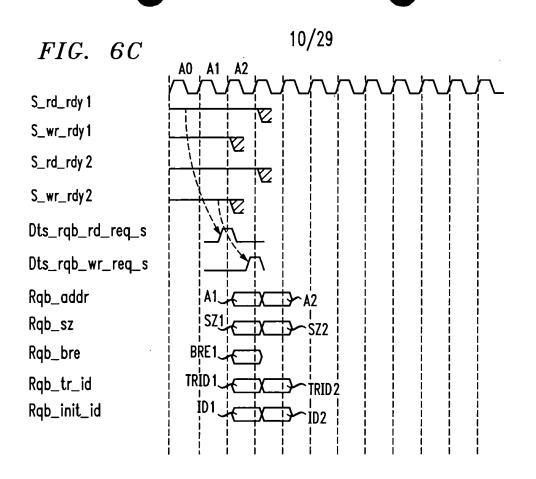
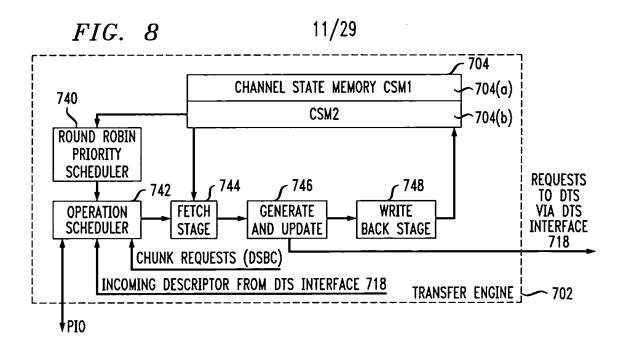
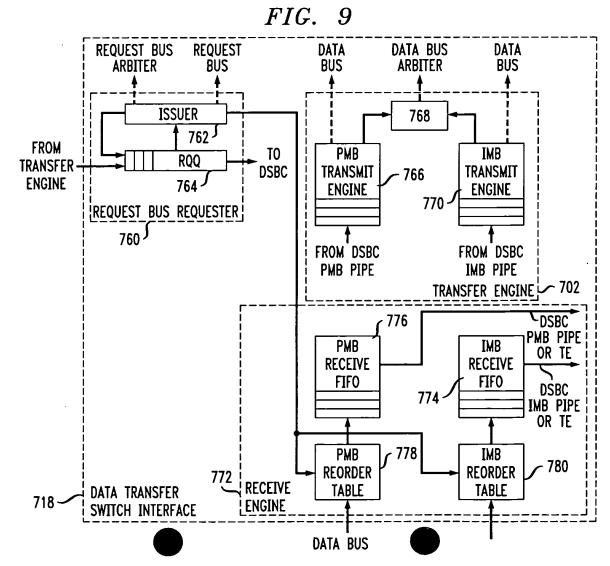


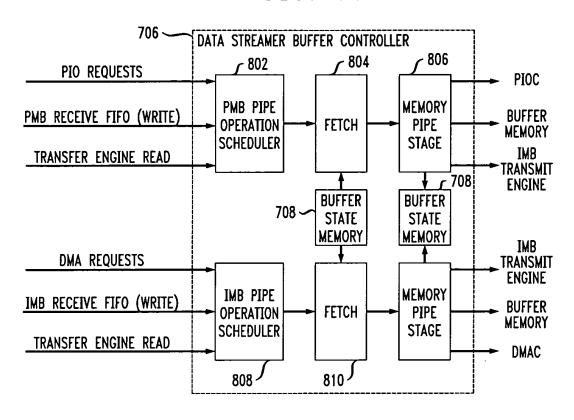
FIG. 7 122 - DATA STREAMER DŤS 718 ~ **INTERFACE** 716 704 702 TRANSFER CHANNEL STATE INTERNAL **MEMORY ENGINE** FIF0 PIO REQUESTS/DATA BUFFER ~ 714a 706 **MEMORY BUFFER STATE** DATA STREAMER BANK 0 ~ 714 BUFFER CONTROLLER **MEMORY** BANK 1 708 -714b I/O BUS ARBITER DMA CONTROLLER **- 138** 710 PIOC I/O BUS

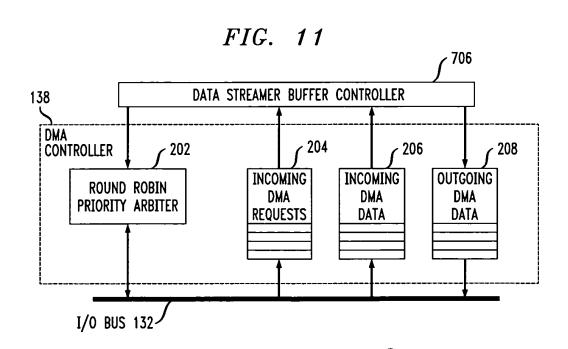




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FIG. 10

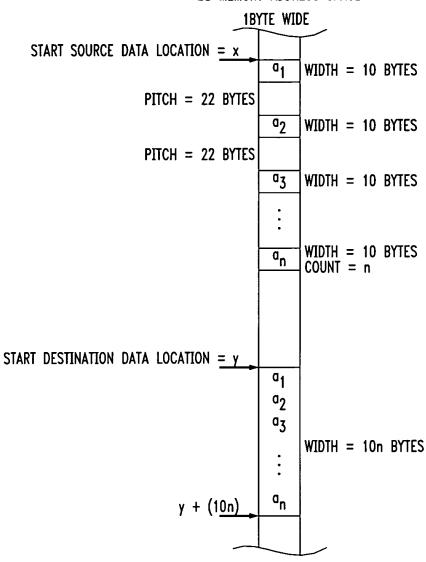




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FIG. 12

4GB MEMORY ADDRESS SPACE



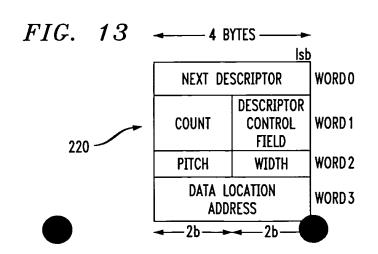
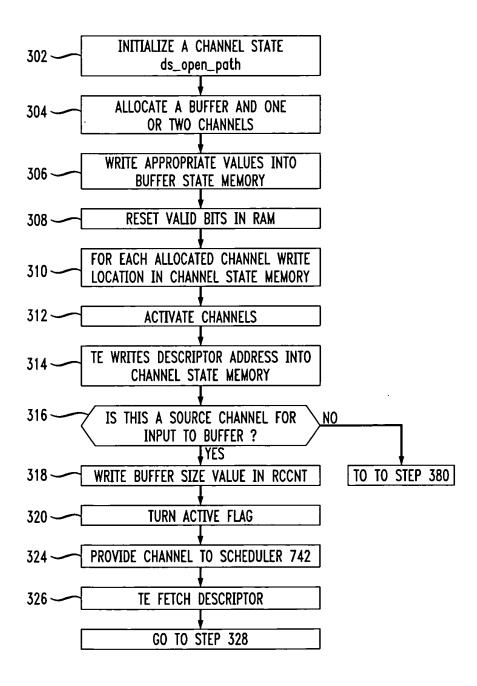
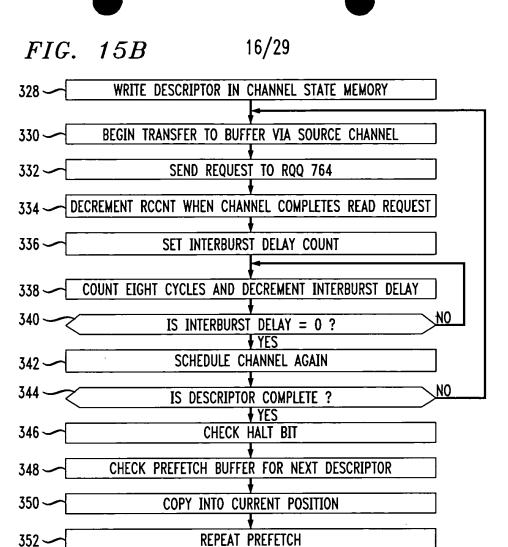


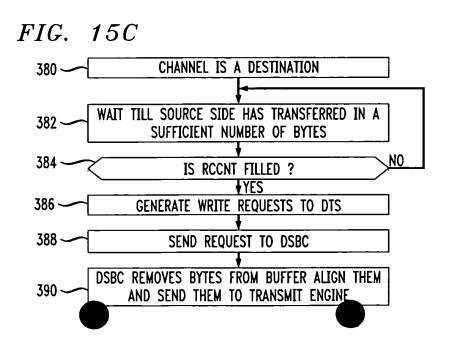
FIG. 14

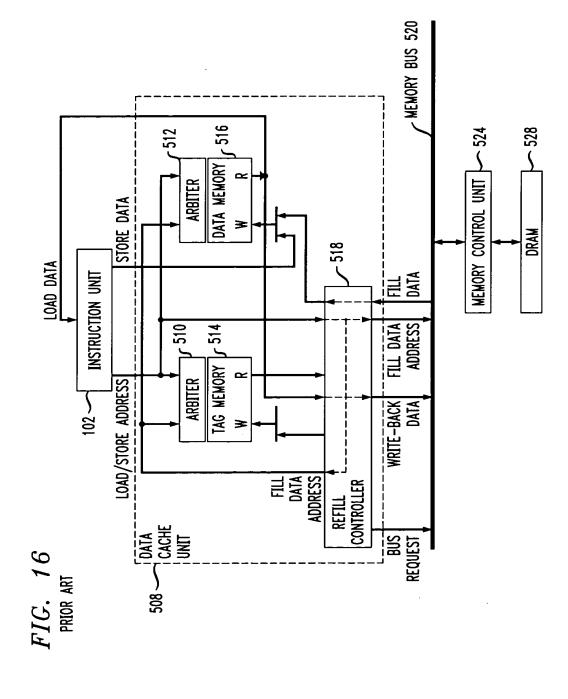
→ 4 BYTES →		
Isb		
NEXT DESCRIPTOR		
COUNT = k	DESCRIPTOR CONTROL FIELD	·
xxxxxx	WIDTH	
DATA LOCATION ADDRESS 1		240
DATA LOCATION ADDRESS 2		
DATA LOCATION ADDRESS 3		
•		
DATA LOCATION ADDRESS k		
<u> </u>		

FIG. 15A









אויונן מיינן מיינן וויין אויין אויין איינן אויין איינן אויין אויין אויין אויין אויין אויין אויין אויין אויין א אין אוין אויין
k.

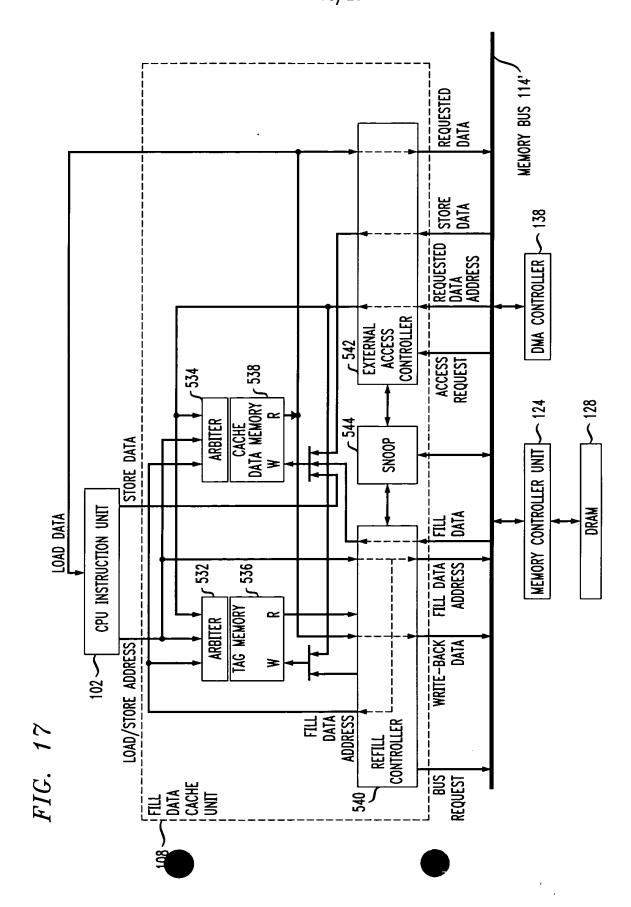


FIG. 18

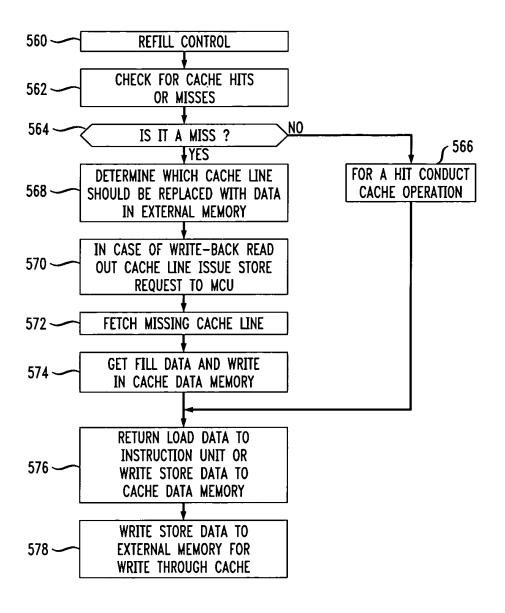
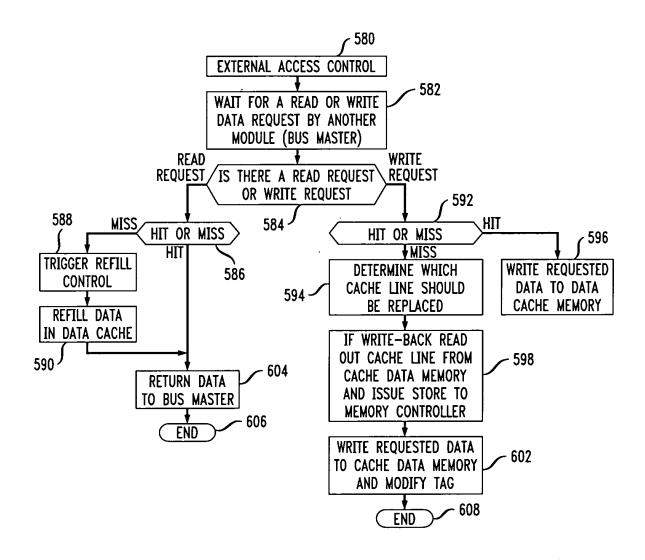


FIG. 19



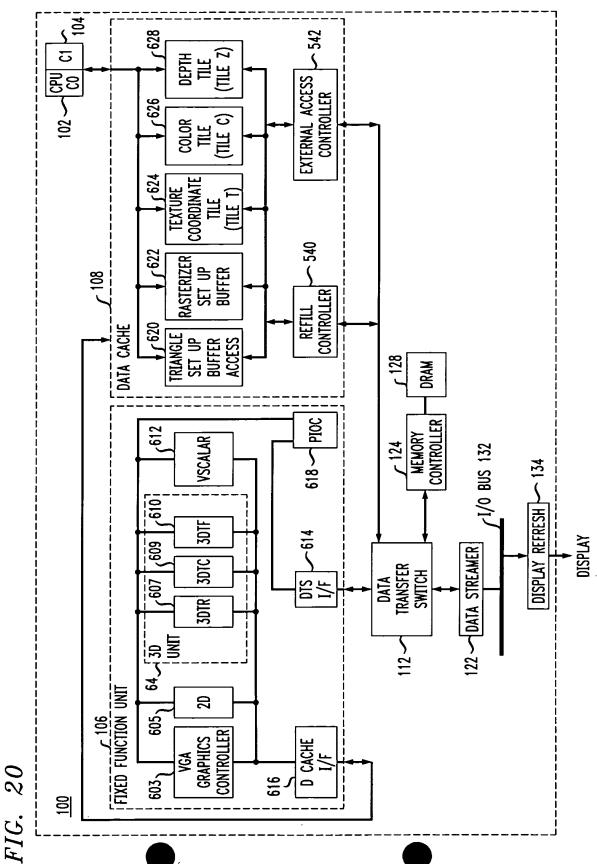


FIG. 21

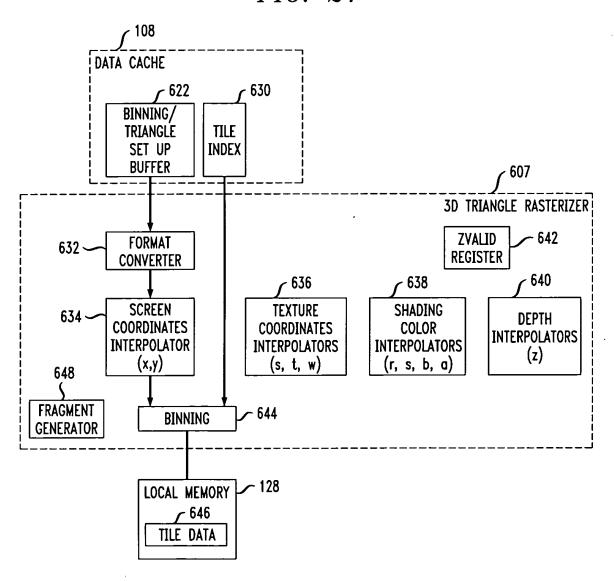


FIG. 22

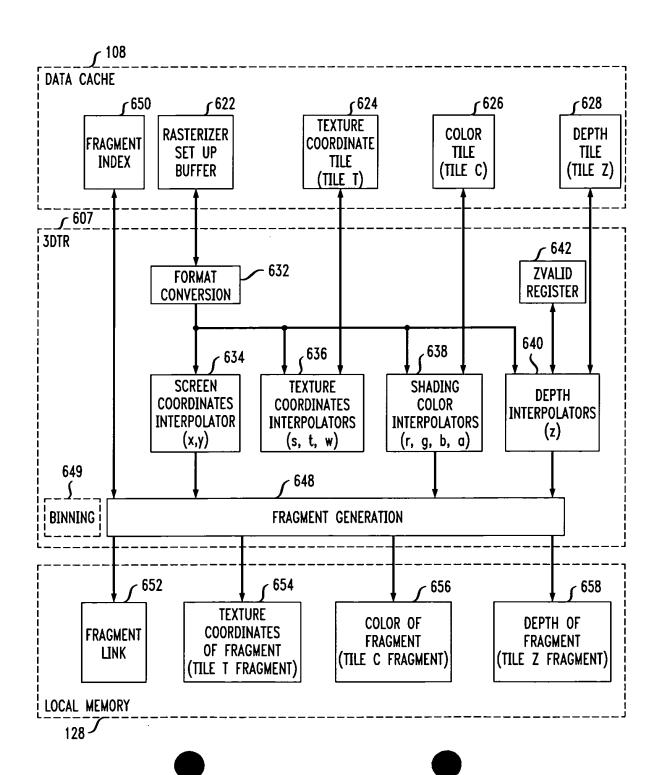


FIG. 23

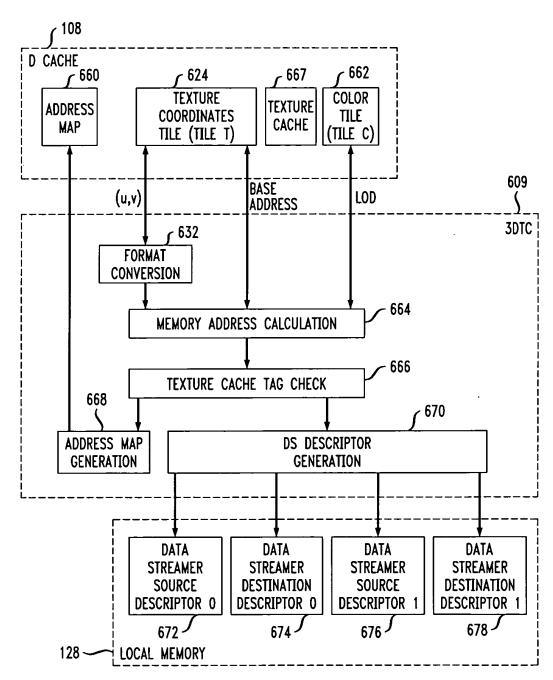
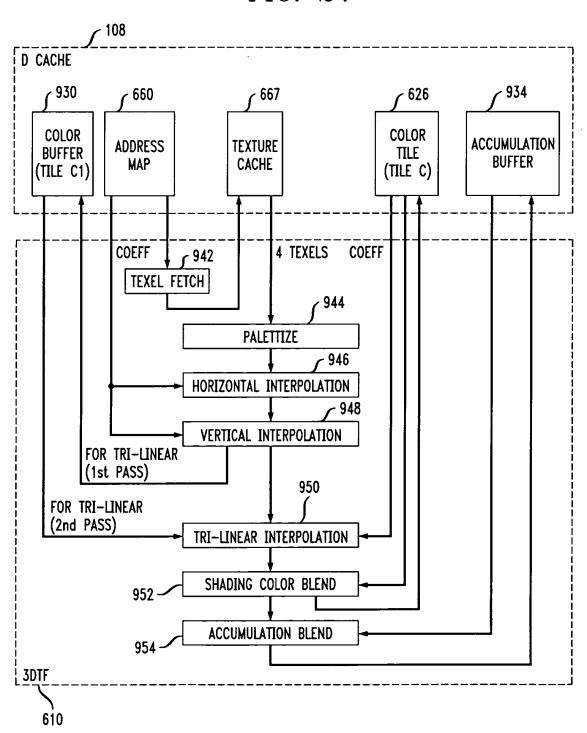
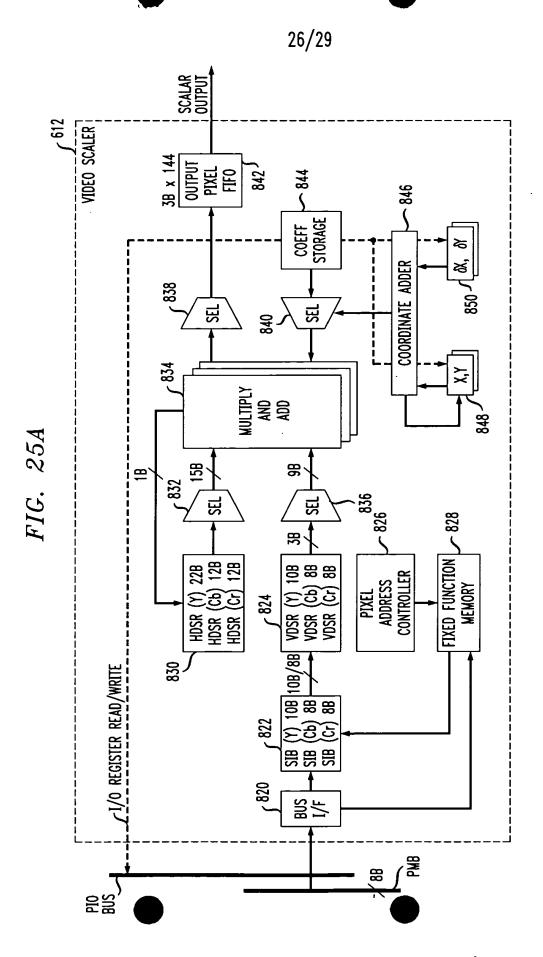


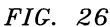
FIG. 24

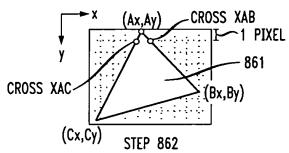


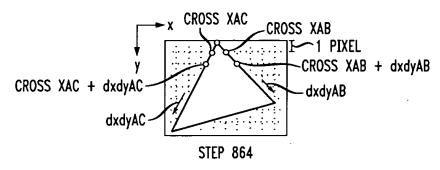


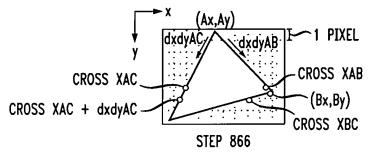
COEFF PIXEL COEFF TEMP FIG. 25B ر 998 COEFF TEMP PIXEL COEFF TEMP PIXEL 854 ر 856 COEFF MULTIPLY AND ADD TEMP 834 PIXEL

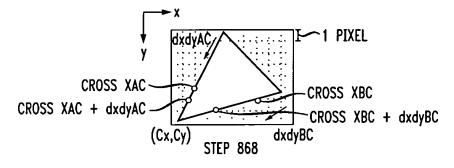
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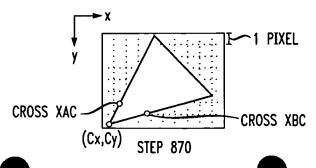












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FIG. 27

